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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig, et al.

Serial No.:

10/066,188

Filing Date:

1/31/02

For:

METHOD AND APPARATUS FOR

SPECIFYING ENCODED SUB-

NETWORKS

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Dated: December 1, 2003

Respectfully

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	(1	use as many sheets a	as nece	ssary)	Examiner Name				
	Sheet	1	of	1	Attorney Docket Number	SPLX.P0095			

				U.S.	PATENT DOCUMENTS	
Examiner* Initials	Cite No.1	Number Kin	cument od Code ² f known)	Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	2.	5,537,341		07-16-1996	Rose et al.	DECENTED
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NON PATENT LITERATURE DOCUMENTS						
Examiner* Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶			
	8.	Crastes, M. et al., A Technology Mapping Method Based on Perfect and Semi-Perfect Matchings, Design Automation Conference, 1991, 28 th ACM/IEEE, June 17-21, 1991, pp. 93-98.				
9. Devadas, S. et al., Synthesis and Optimization for robustly de circuits, Design Automation Conference, 1990, 27 th ACM/IEE		Devadas, S. et al., Synthesis and Optimization for robustly delay-fault testable combinational logic circuits, Design Automation Conference, 1990, 27 th ACM/IEEE, June 24-28, 1990, pp. 221-227.				

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